

CS-401 FILE 1 FOR
MIDTERM BY MOHSIN
CHINIOTI

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Question # 1 of 10 (Start time: 07:48:15 PM, 31 December 2021)

Cell width refers to the total number of bits in a memory cell while the total number of cells is called the_____.

Select the correct option

☐	Cell Length	
☐	Cell Height	
☐	Cell width	CORRECT ANS MOHSIN CHINIOTI 03137248280
☐	Depth	

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Question # 2 of 10 (Start time: 07:49:19 PM, 31 December 2021)

There are _____ forms of the DIV instruction.

Select the correct option

☐	Four	CORRECT ANS MOHSIN CHINIOTI 03137248280
☐	One	
☐	Two	
☐	Three	

Question # 3 of 10 (Start time: 07:50:23 PM, 31 December 2021)

Shift logical Right (SHR) moves all the bits one position to the _____ and inserts a zero from the _____.

► Select the correct option

☐	right, right	
☐	right, left	
☐	left, left	
☐	left, right	CORRECT ANS MOHSIN CHINIOT 03137248280

Question # 4 of 10 (Start time: 07:51:55 PM, 31 December 2021)

The unit of stack operations in iAPX88 is a _____.

▶ Select the correct option

☐	double word	
☐	word	CORRECT ANS MOHSIN CHINIOTI 03137248280
☐	bit	
☐	byte	

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Question # 5 of 10 (Start time: 07:53:38 PM, 31 December 2021)

The stack of iAPX88 works on _____ sized elements.

Select the correct option

☐	byte
☐	nibble
☐	paragraph
☐	word

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Question # 6 of 10 (Start time: 07:55:11 PM, 31 December 2021)

When the operand of DIV instruction is in 16 bits, then the implied dividend will be in _____.

Select the correct option

☐	DX:AX	CORRECT ANS MOHSIN CHINIOTI 03137248280
☐	ES:AX	
☐	DS:BX	
☐	AX	

Question # 6 of 10 (Start time: 07:55:11 PM, 31 December 2021)

When the operand of DIV instruction is in 16 bits, then the implied dividend will be in _____.

Select the correct option

☐	DX:AX	CORRECT ANS MOHSIN CHINIOTI 03137248280
☐	ES:AX	
☐	DS:BX	
☐	AX	

Question # 7 of 10 (Start time: 07:56:31 PM, 31 December 2021)

In XOR operation, the output is 1 if both inputs are _____.

Select the correct option

☒	different
☐	true
☐	false
☐	same

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Question # 9 of 10 (Start time: 07:58:38 PM, 31 December 2021)

In general, width of a memory cell cannot be greater than the width of _____.

▶ Select the correct option

☐	Data Bus	
☐	I/O Bus	CORRECT ANS MOHS CHINIOTI 0313748280
☐	Control Bus	
☐	Address Bus	

Question # 10 of 10 (Start time: 08:00:09 PM, 31 December 2021)

We can convert a digit into its ASCII representation by adding _____ to it.

Select the correct option

☐	0x20	
☐	0x10	
☐	0x30	CORRECT ANS MOHSIN CHINIOTI 03137248280
☐	0x05	

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BH register is a (an) _____ bit register.

Select the correct option

 Reload Math Equations

☐	24	
☐	16	
☐	8	CORRECT ANS MOHSN CHINIOTI 03137248280
☐	32	

Click to Save Answer & Move to Next Question



In case of DIV BX instruction, the quotient is stored in _____ register.

Select the correct option

☐	AL	
☐	DX	
☐	AX	CORRECT ANS MOHSIN CHINOTI 03137248280
☐	BL	

[Click to Save Answer & Move to Next Question](#)

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Question # 1 of 10 (Start time: 08:10:18 PM, 31 December 2021)

The multiplier is stored in _____ bits in Extended Multiplication.

Select the correct option

☐	16	
☐	8	
☐	64	
☐	32	MOHSIN CHINIOTI 03137248280

Question # 1 of 10 (Start time: 08:10:18 PM, 31 December 2021)

The multiplier is stored in _____ bits in Extended Multiplication.

Select the correct option

☐	16	
☐	8	
☐	64	
☐	32	MOHSIN CHINIOTI 03137248280

Question # 2 of 10 (Start time: 08:11:25 PM, 31 December 2021)

MUL instruction performs an unsigned multiplication of the source operand and the _____.

▶ Select the correct option

☐	base
☐	carry
☐	accumulator
☐	word at ES:DI

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Question # 3 of 10 (Start time: 08:12:57 PM, 31 December 2021)

Which of the following instruction is equivalent to multiplying the value of AX by 8?

▶ Select the correct option

☐	MUL AX, 3	CORRECT ANS MOHSIN CHINOTI 03137248280
☐	SAL AX, 8	
☐	SHL AX, 8	
☐	SHL AX, 3	

Question # 4 of 10 (Start time: 08:14:43 PM, 31 December 2021)

Which of the following jump does not depend on flags register?

Select the correct option

☐	JP	
☐	JCXZ	
☐	JO	
☐	JNE	CORRECT ANS MOHSIN CHINIOTI

Question # 5 of 10 (Start time: 08:16:07 PM, 31 December 2021)

The main difference between standard and extended ASCII tables is the number of defined

Select the correct option

☐	I/O ports	
☐	symbols	CORRECT ANS MOHSIN CHINIOTI 03137248280
☐	serial ports	
☐	rows and columns on VGA	

Question # 6 of 10 (Start time: 08:17:42 PM, 31 December 2021)

Program consists of _____ logical parts.

Select the correct option

☐	Three	
☐	Two	CORRECT ANS 03137248280 MOHSIN CHINIOTI
☐	Four	
☐	One	

Question # 7 of 10 (Start time: 08:18:59 PM, 31 December 2021)

To modify the processor's behavior, _____ instructions are used.

Select the correct option

☐	data movement	
☐	program control	
☐	special	
☐	arithmetic logic	CORRECT ANS MOHSIN CHINIOTI 03137248280

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Question # 8 of 10 (Start time: 08:20:20 PM, 31 December 2021)

The ASCII code for digit 8 is _____.

▶ Select the correct option

☐	0x38	CORRECT ANS MOHSIN CHINIOTI 03137248280
☐	0x18	
☐	0x28	
☐	0x80	

Question # 9 of 10 (Start time: 08:21:54 PM, 31 December 2021)

Which of the following is also called intra-segment call?

Select the correct option

☐	Far call
☐	Short call
☐	Long call
☐	Near call

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