

Question No. 01

VESA VBE 2.0 is a standard for _____

- A. High resolution Mode
- B. Low resolution Mode
- C. Medium resolution Mode
- D. Very High resolution Mode

VESA VBE 2.0

- ☐ VESA VBE 2.0 for a standard used for these high resolution modes.
- ☐ VESA - Video Electronics Standards Association
- ☐ VBE - Video BIOS Extensions
- ☐ VESA defined modes is 4117 which is a 1024x768 mode with 16bit color and a linear frame buffer.
- ☐ The 16 color bits for every pixel are organized in 5:6:5 format with 5 bits for red, 6 for green, and 5 for blue.
- ☐ This makes 32 shades of red and blue and 64 shades of green and 64K total possible colors.

Question No. 02

Which flags are NOT used for mathematical operations?

- A. Carry, Interrupt and Trap flag.
- B. Direction, Interrupt and Trap flag.**
- C. Direction, Overflow and Trap flag.
- D. Direction, Interrupt and Sign flag.

Mathematical Operations

- ☐ The three flags not used for mathematical operations
- ☐ These are the direction flag, the interrupt flag and the trap flag.

Question No. 03

When two devices in the system want to use the same IRQ line then what will happen?

- A. An IRQ Collision
- B. An IRQ Crash
- C. An IRQ Conflict**
- D. An IRQ Blockage

Hardware Interrupt (Interrupt Requests - IRQ)

- ☐ An IRQ conflict means that two devices in the system want to use the same IRQ line.
- ☐ Each IRQ is mapped to a specific interrupt in the system..
- ☐ IRQ 0 → Timer Device
- ☐ IRQ 1 → Keyboard Device
- ☐ IRQ 2 → Cascading
- ☐ IRQ 3 → Serial Port (COM 2)
- ☐ IRQ 4 → Serial Port (COM 1)
- ☐ IRQ 5 → Sound Card
- ☐ IRQ 6 → Floppy Disk Drive
- ☐ IRQ 7 → Parallel Port

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Question No. 04

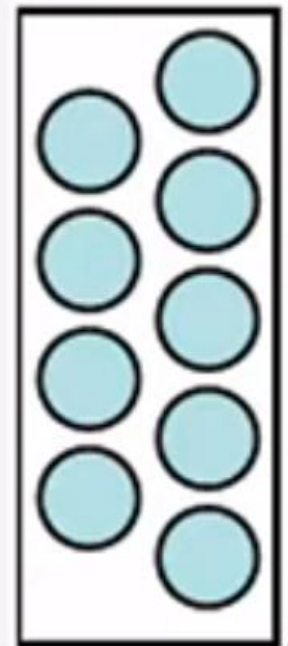
In 9pin DB 9, RI (Ring Indicator) is assigned on pin number

- A. 1
- B. 3
- C. 5
- D. 9**

Serial Port

In parallel port whole bytes are sent from one place to another. In serial port bits are sent one by one. The serial port connection is a 9pin DB-9 connector with pins assigned as shown below.

- 1 – Carrier Detect (CD)
- 2 – Received Data (RD)
- 3 – Transmitted Data (TD)
- 4 – Data Terminal Ready (DTR)
- 5 – Signal Ground (SG)
- 6 – Data Set Ready (DSR)
- 7 – Request to Send (RTS)
- 8 – Clear to Send (CTS)
- 9 – Ring Indicator (RI)



Question No. 05

_____ block processing instructions in 8088.

- A. 1 Block
- B. 3 Block
- C. 5 Block**
- D. 7 Block

String Processing

They are called block processing or string instructions.
There are just 5 block processing instructions in 8088.

The five instructions are STOS, LODS, CMPS, SCAS, and MOVS.

STOS	→ Store String	→ AL:AX to ES:DI
LODS	→ Load String	→ DS:SI to AL:AX
CMPS	→ Compare String	→ DS or SI to ES:DI
SCAS	→ Scan String	→ DS or SI to ES:DI
MOVS	→ Move String	→ DS or SI to ES:DI

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Question No. 06

BIOS sees the disks as _____ storage.

- A. Logical storage
- B. Raw storage page**
- C. In the form of sectors only
- D. In the form of tracks only

Physical Formation

- ☐ BIOS sees the disks as a combination of sectors, tracks, and heads as a raw storage device without concern to whether it is reading a file or directory.

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Question No. 07

A 32bit address register can access up to..... of memory so Memory access has increased a lot.

- A. 8 GB
- B. 6 GB
- C. 4 GB**
- D. 2 GB

386 Architecture

- ☐ All registers in 386 have been extended to 32bits. The new names are EAX, EBX, ECX, EDX, ESI, EDI, ESP, EBP, EIP, and EFLAGS. The original names refer to the lower 16bits of these registers. A 32bit address register can access up to 4GB of memory so memory access has increased a lot.

Question No. 08

The number of pins in a parallel port connector are?

- A. 20
- B. 25**
- C. 30
- D. 35

Parallel Port

Computers can control external hardware through various external ports like the parallel port, the serial port, and the new additions USB and FireWire.

The parallel port connector is a 25pin connector called DB-25. Different pins of this connector have different meanings.

- ☐ Pin 2 to pin 9 that take data from the processor to the device.
- ☐ Pin 10 used as ACK pin, is normally used by the printer to acknowledge.
- ☐ Pin 18-25 are ground and must be connected to the external circuit ground.

Question No. 09

In programmable interrupt controller which of the following ports is used for selectively enabling or disabling interrupts

- A. 19
- B. 20
- C. 21
- D. 22

PIC Port

Programmable interrupt controller has two ports 20 and 21.

- ☐ Port 20 is the control port
- ☐ Port 21 is the interrupt mask register which can be used for selectively enabling or disabling interrupts.

Question No. 10

The source registers in OUT is _____.

- A. AL or AX
- B. BL or BX
- C. CL or CX
- D. DL or DX

IN and OUT Instructions

The source register in OUT and destination register in IN is AL or AX depending on which form is used.

Port numbers for specific devices are fixed by the IBM standard.

For example

- ☐ Port 20 and 21 are for PIC
- ☐ Port 60 to 64 for Keyboard
- ☐ Port 378 for the parallel port
etc.

Question No. 11

VESA stand for _____

- A. Video Electricity Standards Association
- B. Video Electronics Standards Association**
- C. Both A and B
- D. All of the above

VESA VBE 2.0

- ☐ VESA VBE 2.0 for a standard used for these high resolution modes.
- ☐ VESA - Video Electronics Standards Association
- ☐ VBE - Video BIOS Extensions
- ☐ VESA defined modes is 4117 which is a 1024x768 mode with 16bit color and a linear frame buffer.
- ☐ The 16 color bits for every pixel are organized in 5:6:5 format with 5 bits for red, 6 for green, and 5 for blue.
- ☐ This makes 32 shades of red and blue and 64 shades of green and 64K total possible colors.

Question No. 12

IRQ 1 is used for _____.

- A. Timer
- B. Sound Card
- C. Keyboard
- D. Parallel Port

Hardware Interrupt (Interrupt Requests - IRQ)

- ☐ An IRQ conflict means that two devices in the system want to use the same IRQ line.
- ☐ Each IRQ is mapped to a specific interrupt in the system..
- ☐ IRQ 0 → Timer Device
- ☐ IRQ 1 → Keyboard Device
- ☐ IRQ 2 → Cascading
- ☐ IRQ 3 → Serial Port (COM 2)
- ☐ IRQ 4 → Serial Port (COM 1)
- ☐ IRQ 5 → Sound Card
- ☐ IRQ 6 → Floppy Disk Drive
- ☐ IRQ 7 → Parallel Port

Question No. 13

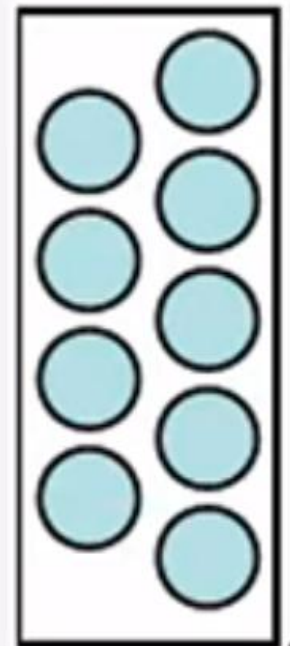
In 9pin DB 9, Signal ground is assigned on pin number

- A. 1
- B. 3
- C. 5
- D. 9

Serial Port

In parallel port whole bytes are sent from one place to another. In serial port bits are sent one by one. The serial port connection is a 9pin DB-9 connector with pins assigned as shown below.

- 1 – Carrier Detect (CD)
- 2 – Received Data (RD)
- 3 – Transmitted Data (TD)
- 4 – Data Terminal Ready (DTR)
- 5 – Signal Ground (SG)
- 6 – Data Set Ready (DSR)
- 7 – Request to Send (RTS)
- 8 – Clear to Send (CTS)
- 9 – Ring Indicator (RI)



Question No. 14

Which of the following pin is used for acknowledgement from printer in parallel port?

- A. 2
- B. 10**
- C. 18
- D. 25

Parallel Port

Computers can control external hardware through various external ports like the parallel port, the serial port, and the new additions USB and FireWire.

The parallel port connector is a 25pin connector called DB-25. Different pins of this connector have different meanings.

- ☐ Pin 2 to pin 9 that take data from the processor to the device.
- ☐ Pin 10 used as ACK pin, is normally used by the printer to acknowledge.
- ☐ Pin 18-25 are ground and must be connected to the external circuit ground.

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Question No. 15

IN and OUT Instructions which port is reserve for Keyboard.

- A. 01 to 19
- B. 20 to 21
- C. 60 to 64
- D. None of the above

IN and OUT Instructions

The source register in OUT and destination register in IN is AL or AX depending on which form is used.

Port numbers for specific devices are fixed by the IBM standard.

For example

- ☐ Port 20 and 21 are for PIC
- ☐ Port 60 to 64 for Keyboard
- ☐ Port 378 for the parallel port etc.

Question No. 16

_____ transfers the word at the current top of stack (pointed to by SP) to the destination operand and then increments SP by two to point to the new top of stack.

- A. PUSH
- B. POP
- C. CALL
- D. All of the above

Push , Pop , Call

PUSH decrements SP (the stack pointer) by two and then transfers a word from the source operand to the top of stack now pointed to by SP.

POP transfers the word at the current top of stack (pointed to by SP) to the destination operand and then increments SP by two to point to the new top of stack.

CALL activates an out-of-line procedure, saving information on the stack to permit a RET (return) instruction in the procedure to transfer control back to the instruction following the CALL

Question No. 17

_____ provides the simplest and most powerful interface to the storage medium.

- A. DOS
- B. BIOS**
- C. Both A and B
- D. All of the above

BIOS and DOS

BIOS provides the simplest and most powerful interface to the storage medium.

DOS builds a logical structure on this raw storage space to provide these abstractions.

Question No. 18

_____ instructions is used to load string in 8088 architecture.

- A. STOS
- B. LODS**
- C. CMPS
- D. SCAS

String Processing

They are called block processing or string instructions. There are just 5 block processing instructions in 8088.

The five instructions are STOS, LODS, CMPS, SCAS, and MOVS.

STOS	→ Store String	→ AL:AX to ES:DI
LODS	→ Load String	→ DS:SI to AL:AX
CMPS	→ Compare String	→ DS or SI to ES:DI
SCAS	→ Scan String	→ DS or SI to ES:DI
MOVS	→ Move String	→ DS or SI to ES:DI

Question No. 19

The time interval between two timer ticks is?

- A. 40ms
- B. 45ms
- C. 50ms
- D. 55ms

Programmable Interval Timer

Another very important peripheral device is the Programmable Interval Timer (PIT), the chip numbered 8254.

- ☐ There is an interval of about 55ms between two timer ticks.
- ☐ The system time is maintained with the timer interrupt.

Question No. 20

PIT Stands for _____

- A. **Programmable Interval Timer**
- B. Program Interval Timer
- C. Programmable Interrupt Timer
- D. All of the above

Abbreviations

PIT	→ Programmable Interval Timer
TSR	→ Terminate and Stay Resident
DOS	→ Disk Operating System
IDT	→ Interrupt Descriptor Table
GDT	→ Global Descriptor Table



Question No. 21

VBE stand for _____

- A. Video BIOS Extensions
- B. Video Basic Extensions
- C. Both A and B
- D. All of the above

VESA VBE 2.0

- ☐ VESA VBE 2.0 for a standard used for these high resolution modes.
- ☐ VESA - Video Electronics Standards Association
- ☐ VBE - Video BIOS Extensions
- ☐ VESA defined modes is 4117 which is a 1024x768 mode with 16bit color and a linear frame buffer.
- ☐ The 16 color bits for every pixel are organized in 5:6:5 format with 5 bits for red, 6 for green, and 5 for blue.
- ☐ This makes 32 shades of red and blue and 64 shades of green and 64K total possible colors.

Question No. 22

IRQ 7 is used for _____.

- A. Timer
- B. Sound Card
- C. Keyboard
- D. Parallel Port

Hardware Interrupt (Interrupt Requests - IRQ)

- ☐ An IRQ conflict means that two devices in the system want to use the same IRQ line.
- ☐ Each IRQ is mapped to a specific interrupt in the system..
- ☐ IRQ 0 → Timer Device
- ☐ IRQ 1 → Keyboard Device
- ☐ IRQ 2 → Cascading
- ☐ IRQ 3 → Serial Port (COM 2)
- ☐ IRQ 4 → Serial Port (COM 1)
- ☐ IRQ 5 → Sound Card
- ☐ IRQ 6 → Floppy Disk Drive
- ☐ IRQ 7 → Parallel Port

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Question No. 23

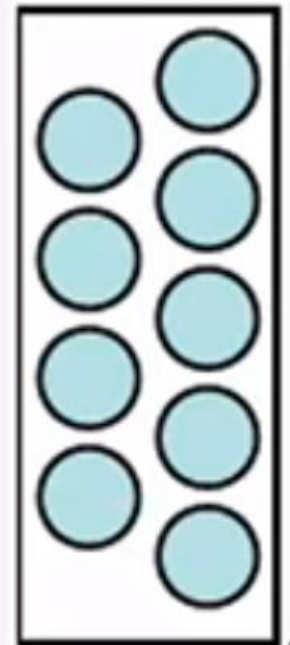
In 9pin DB 9, which pin number is assigned to CD (Carrier Detect)?

- A. 1
- B. 3
- C. 5
- D. 9

Serial Port

In parallel port whole bytes are sent from one place to another. In serial port bits are sent one by one. The serial port connection is a 9pin DB-9 connector with pins assigned as shown below.

- 1 – Carrier Detect (CD)
- 2 – Received Data (RD)
- 3 – Transmitted Data (TD)
- 4 – Data Terminal Ready (DTR)
- 5 – Signal Ground (SG)
- 6 – Data Set Ready (DSR)
- 7 – Request to Send (RTS)
- 8 – Clear to Send (CTS)
- 9 – Ring Indicator (RI)



Question No. 24

_____ builds a logical structure on this raw storage space to provide these abstractions.

- A. DOS
- B. BIOS
- C. Both A and B
- D. All of the above

BIOS and DOS

BIOS provides the simplest and most powerful interface to the storage medium.

DOS builds a logical structure on this raw storage space to provide these abstractions.

Question No. 25

In which instructions transfers a byte or word from the source location DS:SI to the destination AL:AX.

- A. STOS
- B. LODS**
- C. CMPS
- D. SCAS

String Processing

They are called block processing or string instructions. There are just 5 block processing instructions in 8088.

The five instructions are STOS, LODS, CMPS, SCAS, and MOVS.

STOS	→ Store String	→ AL:AX to ES:DI
LODS	→ Load String	→ DS:SI to AL:AX
CMPS	→ Compare String	→ DS or SI to ES:DI
SCAS	→ Scan String	→ DS or SI to ES:DI
MOVS	→ Move String	→ DS or SI to ES:DI

Question No. 26

The system time is maintained with the _____ interrupt.

- A. Time Interrupt
- B. Clock Interrupt
- C. **Timer Interrupt**
- D. All of the above

Programmable Interval Timer

Another very important peripheral device is the Programmable Interval Timer (PIT), the chip numbered 8254.

- ☐ There is an interval of about 55ms between two timer ticks.
- ☐ The system time is maintained with the timer interrupt.

Question No. 27

TSR Stands for _____

- A. **Terminate and Stay Resident**
- B. Terminate and State Resident
- C. Termination and Stay Resident
- D. All of the above

Abbreviations

PIT	→ Programmable Interval Timer
TSR	→ Terminate and Stay Resident
DOS	→ Disk Operating System
IDT	→ Interrupt Descriptor Table
GDT	→ Global Descriptor Table

Question No. 28

The physical address of IDT(Interrupt Descriptor Table) is stored in _____

- A. GDTR
- B. IDTR**
- C. IVT
- D. IDTT

Interrupt Handling

- ☐ IDT (Interrupt Descriptor Table) located at physical address stored in **IDTR**, a special purpose register.
- ☐ The **IDTR** is also a 48bit register similar in structure to the **GDTR** and loaded with another special instruction **LGDT**.

Question No. 29

MS DOS uses _____ display mode.

- A. Console Based
- B. Graphic Based
- C. Numeric Based
- D. Character Based

MS DOS

☐ MS DOS uses **Character Based** display mode.

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Question No. 30

8088 is a _____ bits architecture.

- A. 8 bits
- B. 16 bits**
- C. 32 bits
- D. 64 bits

8088 Architecture

- ☐ 8088 Architecture is a **16 bits** architecture.
- ☐ It perform four basic operations are AND, OR, XOR, and NOT.

Question No. 31

VESA VBE 2.0 is a standard have _____ bits color.

- A. 8 bits
- B. 16 bits**
- C. 32 bits
- D. None of the above

VESA VBE 2.0

- ☐ VESA VBE 2.0 for a standard used for these high resolution modes.
- ☐ VESA - Video Electronics Standards Association
- ☐ VBE - Video BIOS Extensions
- ☐ VESA defined modes is 4117 which is a 1024x768 mode with 16bit color and a linear frame buffer.
- ☐ The 16 color bits for every pixel are organized in 5:6:5 format with 5 bits for red, 6 for green, and 5 for blue.
- ☐ This makes 32 shades of red and blue and 64 shades of green and 64K total possible colors.

Question No. 32

IRQ 6 is used for _____.

- A. Timer
- B. Sound Card
- C. Keyboard
- D. Floppy Disk

Hardware Interrupt (Interrupt Requests - IRQ)

- ☐ An IRQ conflict means that two devices in the system want to use the same IRQ line.
- ☐ Each IRQ is mapped to a specific interrupt in the system..
- ☐ IRQ 0 → Timer Device
- ☐ IRQ 1 → Keyboard Device
- ☐ IRQ 2 → Cascading
- ☐ IRQ 3 → Serial Port (COM 2)
- ☐ IRQ 4 → Serial Port (COM 1)
- ☐ IRQ 5 → Sound Card
- ☐ IRQ 6 → Floppy Disk Drive
- ☐ IRQ 7 → Parallel Port

Question No. 33

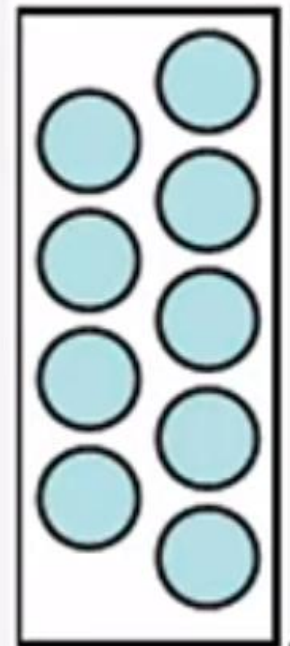
In 9pin DB 9, which pin number is assigned to TD (Transmitted Data)?

- A. 1
- B. 3
- C. 5
- D. 9

Serial Port

In parallel port whole bytes are sent from one place to another. In serial port bits are sent one by one. The serial port connection is a 9pin DB-9 connector with pins assigned as shown below.

- 1 – Carrier Detect (CD)
- 2 – Received Data (RD)
- 3 – Transmitted Data (TD)
- 4 – Data Terminal Ready (DTR)
- 5 – Signal Ground (SG)
- 6 – Data Set Ready (DSR)
- 7 – Request to Send (RTS)
- 8 – Clear to Send (CTS)
- 9 – Ring Indicator (RI)



Question No. 34

BIOS sees the disks as _____.

- A. Sector
- B. Tracks
- C. Heads
- D. All of the above

Physical Formation

- ☐ BIOS sees the disks as a combination of sectors, tracks, and heads as a raw storage device without concern to whether it is reading a file or directory.

Question No. 35

Which of the following pin is used for taking data from processor to the devices in parallel port?

- A. 2
- B. 10
- C. 18
- D. 25

Parallel Port

Computers can control external hardware through various external ports like the parallel port, the serial port, and the new additions USB and FireWire.

The parallel port connector is a 25pin connector called DB-25. Different pins of this connector have different meanings.

- ☐ Pin 2 to pin 9 that take data from the processor to the device.
- ☐ Pin 10 used as ACK pin, is normally used by the printer to acknowledge.
- ☐ Pin 18-25 are ground and must be connected to the external circuit ground.

Question No. 36

_____ activates an out-of-line procedure, saving information on the stack to permit a RET (return) instruction in the procedure to transfer control back to the instruction following the CALL.

- A. PUSH
- B. POP
- C. CALL
- D. All of the above

Push , Pop , Call

PUSH decrements SP (the stack pointer) by two and then transfers a word from the source operand to the top of stack now pointed to by SP.

POP transfers the word at the current top of stack (pointed to by SP) to the destination operand and then increments SP by two to point to the new top of stack.

CALL activates an out-of-line procedure, saving information on the stack to permit a RET (return) instruction in the procedure to transfer control back to the instruction following the CALL

Question No. 37

_____ instructions is used to scan string in 8088 architecture.

- A. STOS
- B. LODS
- C. CMPS
- D. SCAS

String Processing

They are called block processing or string instructions. There are just 5 block processing instructions in 8088.

The five instructions are STOS, LODS, CMPS, SCAS, and MOVS.

STOS	→ Store String	→ AL:AX to ES:DI
LODS	→ Load String	→ DS:SI to AL:AX
CMPS	→ Compare String	→ DS or SI to ES:DI
SCAS	→ Scan String	→ DS or SI to ES:DI
MOVS	→ Move String	→ DS or SI to ES:DI

Question No. 38

DOS Stands for _____

- A. Disk Operating System
- B. Disk Operation System
- C. Disk Operating State
- D. All of the above

Abbreviations

PIT	→ Programmable Interval Timer
TSR	→ Terminate and Stay Resident
DOS	→ Disk Operating System
IDT	→ Interrupt Descriptor Table
GDT	→ Global Descriptor Table

Question No. 39

The IDTR is also _____ bit register.

- A. 16 bits
- B. 32 bits
- C. 48 bits
- D. 64 bits

Interrupt Handling

- ☐ IDT (Interrupt Descriptor Table) located at physical address stored in IDTR, a special purpose register.
- ☐ The IDTR is also a 48bit register similar in structure to the GDTR and loaded with another special instruction LGDT.

Question No. 40

8088 architecture perform which of the following operations.

- A. AND
- B. OR
- C. XOR
- D. All of the above

8088 Architecture

- ☐ 8088 Architecture is a **16 bits** architecture.
- ☐ It perform four basic operations are AND, OR, XOR, and NOT.

Question No. 41

GDT Stands for _____

- A. Global Descriptor Table
- B. Global Description Table
- C. Global Descriptor Timer
- D. All of the above

Abbreviations

PIT	→ Programmable Interval Timer
TSR	→ Terminate and Stay Resident
DOS	→ Disk Operating System
IDT	→ Interrupt Descriptor Table
GDT	→ Global Descriptor Table

Question No. 42

IRQ 5 is used for _____.

- A. Timer
- B. Sound Card**
- C. Keyboard
- D. Floppy Disk

Hardware Interrupt (Interrupt Requests - IRQ)

- ☐ An IRQ conflict means that two devices in the system want to use the same IRQ line.
- ☐ Each IRQ is mapped to a specific interrupt in the system..
- ☐ IRQ 0 → Timer Device
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- ☐ IRQ 2 → Cascading
- ☐ IRQ 3 → Serial Port (COM 2)
- ☐ IRQ 4 → Serial Port (COM 1)
- ☐ IRQ 5 → Sound Card
- ☐ IRQ 6 → Floppy Disk Drive
- ☐ IRQ 7 → Parallel Port

Question No. 43

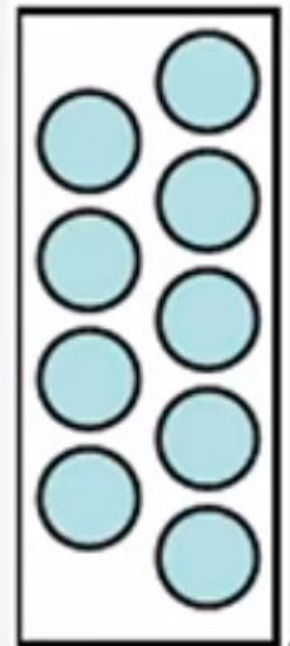
In 9pin DB 9, which pin number is assigned to CTS (Request to Send)?

- A. 1
- B. 3
- C. 5
- D. 7

Serial Port

In parallel port whole bytes are sent from one place to another. In serial port bits are sent one by one. The serial port connection is a 9pin DB-9 connector with pins assigned as shown below.

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- 4 – Data Terminal Ready (DTR)
- 5 – Signal Ground (SG)
- 6 – Data Set Ready (DSR)
- 7 – Request to Send (RTS)
- 8 – Clear to Send (CTS)
- 9 – Ring Indicator (RI)



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Question No. 44

Which of the following pin is used for connected external circuit ground in parallel port?

- A. 2
- B. 10
- C. 18
- D. 25

Parallel Port

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The parallel port connector is a 25pin connector called DB-25. Different pins of this connector have different meanings.

- ☐ Pin 2 to pin 9 that take data from the processor to the device.
- ☐ Pin 10 used as ACK pin, is normally used by the printer to acknowledge.
- ☐ Pin 18-25 are ground and must be connected to the external circuit ground.

Question No. 45

In programmable interrupt controller which of the following ports is used for controlling port.

- A. 19
- B. 20**
- C. 21
- D. 22

PIC Port

Programmable interrupt controller has two ports 20 and 21.

- ☐ Port 20 is the control port
- ☐ Port 21 is the interrupt mask register which can be used for selectively enabling or disabling interrupts.

Question No. 46

_____ instructions is used to store string in 8088 architecture.

- A. STOS
- B. LODS
- C. CMPS
- D. SCAS

String Processing

They are called block processing or string instructions. There are just 5 block processing instructions in 8088 architecture.

The five instructions are STOS, LODS, CMPS, SCAS, and MOVS.

STOS	→ Store String	→ AL:AX to ES:DI
LODS	→ Load String	→ DS:SI to AL:AX
CMPS	→ Compare String	→ DS or SI to ES:DI
SCAS	→ Scan String	→ DS or SI to ES:DI
MOVS	→ Move String	→ DS or SI to ES:DI



Question No. 47

IDT Stands for _____

- A. Interruption Descriptor Table
- B. Interruption Description Table
- C. Interrupt Descriptor Table
- D. All of the above

Abbreviations

PIT	→ Programmable Interval Timer
TSR	→ Terminate and Stay Resident
DOS	→ Disk Operating System
IDT	→ Interrupt Descriptor Table
GDT	→ Global Descriptor Table



Question No. 48

In VESA VBE 2.0 is a standard every pixel are organized in _____ format.

- A. 5 : 5 : 5
- B. 6 : 5 : 6
- C. 5 : 6 : 5
- D. 6 : 6 : 6

VESA VBE 2.0

- ☐ VESA VBE 2.0 for a standard used for these high resolution modes.
- ☐ VESA - Video Electronics Standards Association
- ☐ VBE - Video BIOS Extensions
- ☐ VESA defined modes is 4117 which is a 1024x768 mode with 16bit color and a linear frame buffer.
- ☐ The 16 color bits for every pixel are organized in 5:6:5 format with 5 bits for red, 6 for green, and 5 for blue.
- ☐ This makes 32 shades of red and blue and 64 shades of green and 64K total possible colors.

Question No. 49

In graphics mode a location in video memory corresponds to a on the screen.

- A. Line
- B. Dot**
- C. Circle
- D. Rectangle

Graphic Mode

In graphics mode a location in video memory corresponds to a on the screen in shape of **DOT**.

Question No. 50

Creation of threads can be _____.

- A. **Dynamic**
- B. Statics
- C. Easy
- D. Difficult

Threads

Creation of threads can be **Dynamic**.

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