

VIRTUAL UNIVERSITY OF PAKISTAN

A Good Education is a Foundation For a Better Future

CORRECT ANSWER SOLVED BY HADI
EMAIL: hadirajputofficial@gmail.com
WHATSAPP& CALL: 03087122922

HADI VU PAST PAPER

MID TERM PAST PAPER

We Deal All Kind of VU Project
(PHP, .NET, ANDROID, PYTHON)

B.ED (THESIS AND RESEARCH PROJECT)

MADE AND SOLVED BY HADI

WARNING: Team HADI is not responsible for any mistake or wrong answer. All students reading and using this document may check and confirm the answers at their own.



WhatsApp: 03087122922



FACEBOOK ID: <https://www.facebook.com/hadipastpapers/>

Best of luck!



Question # 4 of 10 (Start time: 08:05:45 PM, 29 June 2022)

Total Marks: 1

In pipe-lined processors, there should be a _____ port register file so that if the register write and register read stages overlap, they can be performed in parallel.

Select the correct option

☐	Two
☐	One
☒	Three
☐	Four

MADE BY HADI

Email:hadirajputofficial@gmail.com WhatsApp:03087122922 (Bitout Software House)

Question # 7 of 10 (Start time: 08:07:41 PM, 29 June 2022)

Total Marks: 1

_____ hazard occurs when attempting to access the same resource in different ways at the same time

Select the correct option

☐	Branch
☒	Structural
☐	Data
☐	Instruction

MADE BY HADI

Email:hadirajputofficial@gmail.com WhatsApp:03087122922 (Bitout Software House)

Question # 1 of 10 (Start time: 08:02:44 PM, 29 June 2022)

Total Marks: 1

In a processor, _____ is responsible for the synchronization of internal as well as external events.

Select the correct option

☒	Control Unit
☐	Data Unit
☐	Arithmetic & Logical Unit
☐	Memory Unit

MADE BY HADI

Email: hadinutofficial@gmail.com WhatsApp: 9308712202 (Bitout Software House)

Question # 2 of 10 (Start time: 08:04:04 PM, 29 June 2022)

Total Marks: 1

_____ provides a temporary storage for the address of memory location to be accessed.

Select the correct option

☒	MAR
☐	MBR
☐	LPC
☐	PC

MADE BY HADI

Email:hadirajputofficial@gmail.com WhatsApp:03087122922 (Bitout Software House)

Question # 3 of 10 (Start time: 08:04:54 PM, 29 June 2022)

Total Marks:

There are _____ types of reset operations in SRC.

Select the correct option



Five



Two



Four



Three

MADE BY HADI

Email:hadicaipuofficial@gmail.com WhatsApp:03087122922 (Bitout Software House)

Question # 9 of 10 (Start time: 08:09:43 PM, 29 June 2022)

Total Marks: 1

For any of the instructions that are a part of the instruction set of the SRC, there are certain _____ required; which may be used to select the appropriate function for the ALU to be performed, to select the appropriate registers, or the appropriate memory location.

Select the correct option

☐	None of the given
☒	Control signals
☐	Memory
☐	Registers

MADE BY HADIEmail: hadirajputofficial@gmail.com WhatsApp: 03087122922 (Bitout Software House)

Question # 6 of 10 (Start time: 08:07:14 PM, 29 June 2022)

Total Marks: 1

The _____ instruction is completed once memory access has been made and the memory location has been written to.

Select the correct option

☒	Store
☐	Branch
☐	Link
☐	Control

MADE BY HADI

Email:hadirajputofficial@gmail.com WhatsApp:03087122922 (Bitout Software House)

Question # 10 of 10 (Start time: 08:11:18 PM, 29 June 2022)

Total Marks: 1

Which are the two control unit design approaches?

Select the correct option

☐

Cache and RAM

☐

Control signals and busses

☒

Hardwired and micro programmed

☐

Registers and instructions

MADE BY HADI

Email:hadirajputofficial@gmail.com WhatsApp:03087122922 (Bitout Software House)

HADI VU PAST PAPER

WE DEAL ALL KIND OF VU PROJECT
(PHP, ANDROID, PYTHON)

DEAR STUDENTS

ARE YOU LOOKING
FOR ASSISTANCE

- 1) ASSIGNMENTS
- 2) QUIZZES
- 3) GDB
- 4) GRAND QUIZZES

We Provide, You Solution
of VU Academic Activities
include Assignments,
Quizzes, GDBs, Grand Quizzes
If You Need Help in Services
Kindly inform us.

TEAM HADI
BITOUT SOFTWARE HOUSE



TEAM HADI
(VU LAHORE)



HADI TECH

#PAID_TASK

CONTACT US

CALL & WHATSAPP

0308-7122922