



CS501 - Advance Computer Architecture



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100% Correct Solve (Alhamdulillah)

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https://www.youtube.com/channel/UCjboEX65C_yN7as5di1OuIQ/playlists Sadiq Ahmed



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Question # 1 of 10 (Start time: 06:00:39 PM, 29 January 2021)

In the little-endian format exchanging data between computers, the data transmitted by one will be received in a "swapped" form by the other.

Select the correct option

☐	Organized
☐	Signals
☒	Swapped
☐	Arranged



CS501:Quiz # 1

Question # 2 of 10 (Start time: 06:01:04 PM, 29 January 2021)

When a particular sector is found, the data is transferred to _____.

Select the correct option

- | | |
|----------------------------------|----------------------|
| ☐ | RAM |
| ☒ | I/O module |
| ☐ | Cache memory |
| ☐ | Instruction register |

Click to





CS501:Quiz # 1

Question # 3 of 10 (Start time: 06:01:24 PM, 29 January 2021)

Tri-state buffers are used for removing _____.

Select the correct option

- | | |
|----------------------------------|------------------------|
| ☐ | Instruction collision |
| ☐ | bus collision |
| ☐ | Instruction contention |
| ☒ | bus contention |





Question # 4 of 10 (Start time: 06:01:44 PM, 29 January 2021)

Total

In which one of the following methods, does the CPU poll to identify the interrupting module and branches to an interrupt service routine on detecting an interrupt?

Select the correct option

☐	Daisy Chain
☒	Software Poll
☐	Multiple interrupt lines
☐	All of the given options

Click to Save Answer & Move to Next Que





Question # 5 of 10 (Start time: 06:02:07 PM, 29 January 2021)

A component connected to the system bus and having control of it during a particular bus cycle is called_____.

Select the correct option

- | | |
|----------------------------------|------------------|
| ☐ | Slave component |
| ☒ | Master component |
| ☐ | System bus |
| ☐ | Buffer component |

Click to Save An





Question # 6 of 10 (Start time: 06:02:25 PM, 29 January 2021)

Most parallel I/O ports used with peripheral devices are mapped on a range of _____.

Select the correct option

- | | |
|----------------------------------|----------------------|
| ☐ | Bus addresses |
| ☐ | Direct memory Access |
| ☒ | contiguous addresses |
| ☐ | Cache |

Click to Save





CS501:Quiz # 1

Quiz

Question # 7 of 10 (Start time: 06:02:47 PM, 29 January 2021)

Interrupt driven I/O is better than _____.

Select the correct option

- | | |
|----------------------------------|--------------------|
| ☒ | Polling |
| ☐ | Data Forwarding |
| ☐ | Stall |
| ☐ | First In First Out |

Click to Save





Question # 8 of 10 (Start time: 06:03:06 PM, 29 January 2021)

Identify the following type of serial communication error condition:
"The prior character that was received was not still read by the CPU and is over written by a new received character."

Select the correct option

☐	Framing error
☐	Parity error
☒	Overrun error
☐	Under-run error





CS501:Quiz # 1

Question # 9 of 10 (Start time: 06:03:27 PM, 29 January 2021)

Select the parts of a hard disk.

Select the correct option

☐	Header only
☐	Data section and a trailer
☐	Data section only
☒	Header, data section and a trailer.





CS501:Quiz # 1

Quiz

Question # 10 of 10 (Start time: 06:03:52 PM, 29 January 2021)

The source file of FALSIM should contain _____ text only.

Select the correct option

☐	Unicode
☒	ASCII
☐	ANSI
☐	UTF

Click to Save

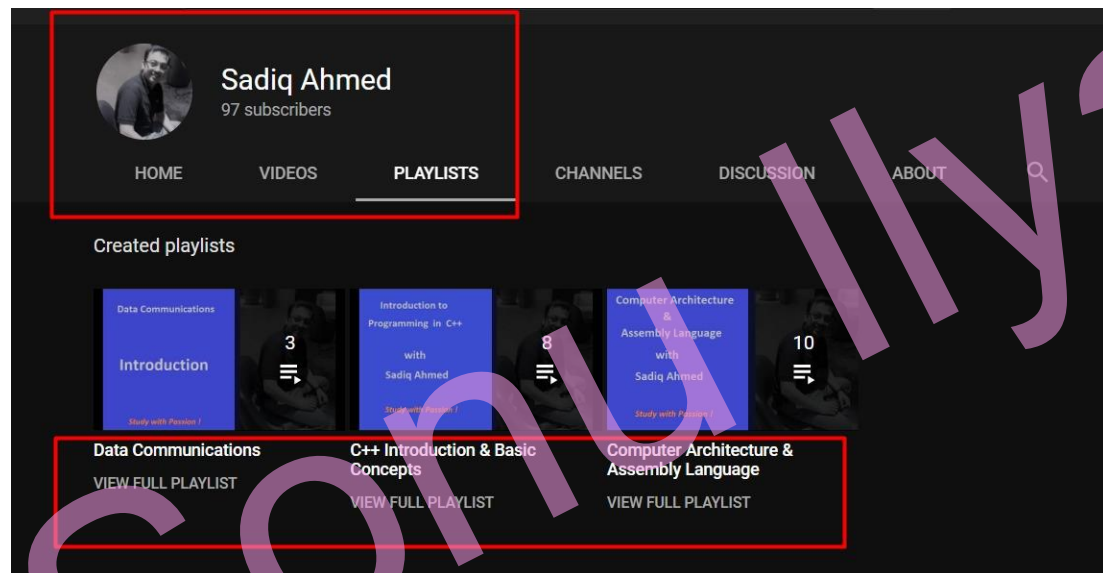




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Subjects Enrolment

- 1) Lecture live on Zoom
- 2) Written Lectures (pdf)
- 3) Recorded Videos
- 4) Topic wise Notes
- 5) Short QA Solved
- 6) MCQs Solved
- 7) Solved Past and Current paper
- 8) Solved Graded Activities (Assignment Quiz GDB)

Activity
Detailed
Information

Project Enrolment Complete training with Live classes on Zoom

- 1) SRS (Software Requirements Specification)
- 2) DD (Design Document)
- 3) Test phase
- a) Test phase viva (Live Classes for viva preparation)
- 4) Final Deliverable
- a) Final coding
- b) Final presentation
- c) Final report
- d) Pre final viva (Coding Viva)
- e) Final viva (Overall Presentation)

*First See Our Result
Than Join Us!*

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