

CS604 Quiz

Last Date 23 Feb 2022

VU Stars

WhatsApp: 03066295623

For paid quizzes

CS604 - Operating Systems (Quiz No. 2)

Question # 3 of 10 (Start time: 04:20:00 PM, 23 February 2022)

In Overlay technique, we can overload any part of the program with the part of the program required needed recently.

Select the correct option

☐	False
☒	True

CS604 - Operating Systems (Quiz No. 2)

Quiz Start Time: 04:18 PM

Question # 4 of 10 (Start time: 04:21:28 PM, 23 February 2022)

Assume a logical address space of 16 pages of 1024 words, each mapped into a physical memory of 32 frames. Each word consists of 2 bytes. What will be the total number of bits required for p (page number)?

Select the correct option

☐	32 bits
☐	8 bits
☒	4 bits
☐	16 bits

[Click to Save Answer & Move to Next Question](#)

CS604 - Operating Systems (Quiz No. 2)

Question # 6 of 10 (Start time: 04:24:04 PM, 23 February 2022)

A page table needed for keeping track of pages of the page table is called -----

Select the correct option

☐	Page size
☐	Page table size
☒	Page directory
☐	2-level paging

Question # 7 of 10 (Start time: 04:24:44 PM, 23 February 2022)

----- is caused due to un-used space in physical memory.

Select the correct option

- | | |
|----------------------------------|------------------------|
| ☐ | MVT |
| ☐ | External fragmentation |
| ☐ | Paging |
| ☒ | Internal fragmentation |

CS604 - Operating Systems (Quiz No. 2)

Question # 8 of 10 (Start time: 04:25:27 PM, 23 February 2022)

A program can not execute unless whole or necessary part of it resides in the main memory.

Select the correct option

☐	False
☒	True

CS604 - Operating Systems (Quiz No. 2)

Question # 9 of 10 (Start time: 04:26:18 PM, 23 February 2022)

-----holds the smallest legal physical memory address for a process

Select the correct option

☒	Base register
☐	Stack pointers register
☐	Index register
☐	Limit register

CS604 - Operating Systems (Quiz No. 2)

Question # 10 of 10 (Start time: 04:26:40 PM, 23 February 2022)

The main memory is usually divided into partitions, one for _____ and other for _____ .

Select the correct option

- | | |
|----------------------------------|----------------------------------|
| ☐ | Operating system, CPU |
| ☐ | Processes, Virtual Memory |
| ☐ | Base Register, Limit Register |
| ☒ | Operating System, User processes |

Question # 1 of 10 (Start time: 04:29:17 PM, 23 February 2022)

Each page is a power of ----- bytes long in paging scheme.

Select the correct option

☐	3
☐	2
☐	5
☒	4

Question # 2 of 10 (Start time: 04:30:07 PM, 23 February 2022)

Physical memory is broken down into fixed-sized blocks, called----- and Logical memory is divided into blocks of the same size, called -----

Select the correct option

☐	Holes, segments
☐	Frames, holes
☐	Pages, Frames
☒	Frames, pages

[Click to Save Answer](#)

Question # 4 of 10 (Start time: 04:31:58 PM, 23 February 2022)

Segmentation is a memory management scheme that supports _____?

Select the correct option

☒	Programmer's view of memory
☐	Hardware's view of memory
☐	None of the given
☐	System's view of memory

Question # 5 of 10 (Start time: 04:32:32 PM, 23 February 2022)

Overlays are implemented by the _____

Select the correct option

☐	Shell
☐	Operating system
☐	Kernel
☒	Programmer

Not sure

Question # 6 of 10 (Start time: 04:33:44 PM, 23 February 2022)

-----holds the smallest legal physical memory address for a process

Select the correct option

☐	Limit register
☐	Stack pointers register
☐	Index register
☒	Base register

Question # 7 of 10 (Start time: 04:34:03 PM, 23 February 2022)

_____ is caused due to un-used space in fixed size blocks/ pages.

Select the correct option

☐	MVT
☒	Internal fragmentation
☐	Paging
☐	External fragmentation

Question # 8 of 10 (Start time: 04:34:51 PM, 23 February 2022)

The system maintains a _____ of all processes whose memory images are on the backing store or in memory and are ready to run.

Select the correct option

☐	List
☐	Environment
☐	Queue
☒	Ready Queue

[Click to Save](#)

Question # 9 of 10 (Start time: 04:35:17 PM, 23 February 2022)

Total Marks: 1

The address generated by the CPU, after any indexing or other addressing-mode arithmetic, is called a -----address, and the address it gets translated to by the MMU is called a -----address.

Select the correct option

☐	Valid, invalid
☒	Virtual, physical
☐	Hexadecimal, Binary
☐	Physical, Virtual

Question # 10 of 10 (Start time: 04:36:31 PM, 23 February 2022)

The ----- protection scheme guards against a process trying to access a page that does not belong to its address space.

Select the correct option

☐	Hybrid
☐	Tertiary
☒	Primary
☐	Secondary

Question # 1 of 10 (Start time: 04:18:37 PM, 23 February 2022)

Every -----generated by the CPU is divided into two parts: a page number (p) and a page offset (d).

Select the correct option

- | | |
|----------------------------------|-----------------------|
| ☐ | Process address space |
| ☐ | Page |
| ☒ | Logical address |
| ☐ | Physical address |

CS604 - Operating Systems (Quiz No. 2)

Question # 2 of 10 (Start time: 04:19:29 PM, 23 February 2022)

Logical memory is divided into blocks of the same size, called _____ .

Select the correct option

☒	Pages
☐	Table
☐	Frame
☐	Page size

