03063548328 Sonu Ilyas Mughal

CS302-Digital Logic Design

SONU ILYAS MUGHAL

Grand Quiz 2020 (Covid-19)

100% Correct Solve (Alhamdulilah)

BY Virtual Assistance Sonu Ilyas Mughal

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CS302:Grand Quiz

Quiz Start Time:

Question # 1 of 30 (Start time: 05:05:36 PM, 28 December 2020)

A _____ output can change from one state to the other by applying appropriate inputs.

Select the correct option

- flip flop
- latch
- counter
- system

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Quiz Start Time: 05:

Question # 2 of 30 (Start time: 05:06:04 PM, 28 December 2020)

A function represented by an expression in _____ form can be readily programmed.

Select the correct option

- (●) Standard SOP
- ( ) Non-standard SOP
- ( ) Boolean
- ( ) Standard POS

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Quiz S

Question # 3 of 30 (Start time: 05:06:30 PM, 28 December 2020)

Portable devices that run on batteries use _____ circuits that have low power dissipation.

Select the correct option

- ○ Series
- ◉ Integrated
- ○ Parallel
- ○ Electric

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Question # 4 of 30 (Start time: 05:06:57 PM, 28 December 2020)

Removing the NOT gate at the output of the NOR gate results in an _____.

Select the correct option

- [x] OR Gate
- [ ] NAND Gate
- [ ] AND Gate
- [ ] NOT Gate

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Question # 5 of 30 (Start time: 05:07:29 PM, 28 December 2020)

The _____ gate and ____ gate implementation connected at the B input of the 4-bit Adder is used to allow Complemented or Un-Complemented B input to be connected to the Adder input.

Select the correct option

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- ○ AND, NOR
- ○ AND, NOT
- ◉ AND, OR
- ○ XOR, NAND

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Quiz Start Time: 05:05 PM, 28 Decemb

Question # 6 of 30 (Start time: 05:07:53 PM, 28 December 2020)

Total M

"The complement of a product of variables is equal to the sum of the complements of the variables." is known as:

Select the correct option

- Demorgan's First Theorem
- Demorgan's Second Theorem
- Boolean Algebra Rule
- Boolean Expression

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Question # 7 of 30 (Start time: 05:08:18 PM, 28 December 2020)

The bolean expression A + BC equals

Select the correct option

- (A' + B)(A' + C)
- (A + B')(A + C')
- (A + B)(A + C)
- (A + B)(A' + C)

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Quiz Start Time: 05:05 PM

Question # 8 of 30 (Start time: 05:08:43 PM, 28 December 2020)

If odd-Parity is being used therefore the 4-bit data and the parity bit should add up to give _____of 1s.

Select the correct option

- ○ Even
- ◉ Odd
- ○ Prime
- ○ Odd and Prime both

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Question # 9 of 30 (Start time: 05:09:08 PM, 28 December 2020)

The AND Gate performs a logical _____function

Select the correct option

- ○ Addition
- ○ Subtraction
- ● Multiplication
- ○ Division

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Quiz Start Time:

Question # 10 of 30 (Start time: 05:09:32 PM, 28 December 2020)

_____ Device dissipate varying amount of power depending upon the frequency of operation.

Select the correct option

- ○ TTL
- ◉ CMOS
- ○ Storage
- ○ Peripheral

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Quiz Start Time: 05:05 PM, 28 De

Question # 11 of 30 (Start time: 05:10:02 PM, 28 December 2020)

If the number of samples that are collected is reduced by half, the reconstructed signal will be _____ from/to the original.

Select the correct option

- (●) Different
- ( ) Same
- ( ) Equal
- ( ) Opposite

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Question # 12 of 30 (Start time: 05:10:26 PM, 28 December 2020)

The Boolean expression (AB'CD')is_____

Select the correct option

- a sumterm
- a product term
- a literal term
- a maxterm

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Quiz Start T

Question # 13 of 30 (Start time: 05:10:50 PM, 28 December 2020)

_____ methods are used to Convert Decimal fractions to Binary.

Select the correct option

- 1
- 2
- 3
- 4

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Question # 14 of 30 (Start time: 05:11:09 PM, 28 December 2020)

A.(B + C) = A.B + A.C is the expression of _____

Select the correct option

- ○ Demorgan's Law
- ○ Commutative Law
- ◉ Distributive Law
- ○ Associative Law

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Quiz Start Time: 05:05

Question # 15 of 30 (Start time: 05:11:36 PM, 28 December 2020)

_____ is used when the output is connected back to the input of the PAL or if the output pin is used as an input only.

Select the correct option

- (●) Combinational Input/Output
- ( ) Combinational Output
- ( ) Combinational Input
- ( ) Programmable polarity

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Question # 16 of 30 (Start time: 05:12:00 PM, 28 December 2020)

Which of the following is the hexadecimal equivalent of 39?

Select the correct option

- 24
- 25
- 26
- 27

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Question # 17 of 30 (Start time: 05:12:34 PM, 28 December 2020)

The product terms in Standard SOP are called_____

Select the correct option

- ○ Midterms
- ◉ Minterms
- ○ Maxterms
- ○ Mixterms

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Question # 18 of 30 (Start time: 05:13:00 PM, 28 December 2020)

A circuit that converts the n inputs to 2^n outputs is called_____.

Select the correct option

- ○ Encoder
- ◉ Decoder
- ○ Comparator
- ○ Shifter

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Question # 19 of 30 (Start time: 05:13:21 PM, 28 December 2020)

_____ is the example of Commutative Law for Multiplication

Select the correct option

- (●) AB=BA
- ( ) A+B=B+A
- ( ) AB+C = A+BC
- ( ) A(B+C) = B(A+C)

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Question # 20 of 30 (Start time: 05:13:45 PM, 28 December 2020)

Each Octal Number digit can represent a _____ Binary Number

Select the correct option

- ○ 2-bit
- ◉ 3-bit
- ○ 4-bit
- ○ 7-bit

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Question # 21 of 30 (Start time: 05:14:11 PM, 28 December 2020)

1011 - 101 = _____

Select the correct option

- 1100
- 0110
- 0011
- 1001

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Quiz Start Time:

Question # 22 of 30 (Start time: 05:14:36 PM, 28 December 2020)

The output of the expression F=A.B.C will be Logic _____ when A=1, B=0, C=1.

Select the correct option

- ○ Undefined
- ○ One
- ● Zero
- ○ No Output as input is invalid.

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Question # 23 of 30 (Start time: 05:14:59 PM, 28 December 2020)

In 32-bit Single-Precision Floating Point format representation the range of exponent value is from _____ to _____

Select the correct option

- (●) +127 to -126
- ( ) +255 to - 254
- ( ) +128 to -127
- ( ) +256 to -255

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Question # 24 of 30 (Start time: 05:15:26 PM, 28 December 2020)

The _____ Gate is used in applications where the output signal is a 1 when any one input is a 1.

Select the correct option

- [x] OR
- [ ] AND
- [ ] NOT
- [ ] XOR

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Question # 25 of 30 (Start time: 05:15:47 PM, 28 December 2020)

Which of the following is the example of comparator?

Select the correct option

- ○ OR
- ○ AND
- ○ XOR
- ◉ XNOR

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Quiz Start Time: 05:05 PM

Question # 26 of 30 (Start time: 05:16:14 PM, 28 December 2020)

A + B = B + A is _____

Select the correct option

- Demorgan's Law
- Distributive Law
- Commutative Law
- Associative Law

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Question # 27 of 30 (Start time: 05:16:37 PM, 28 December 2020)

+ is written in ABEL as_____ .

Select the correct option

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- @
- !
- #
- $

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Question # 28 of 30 (Start time: 05:16:56 PM, 28 December 2020)

A 5-variable karnaugh map has

Select the correct option

- ○ Eight cells
- ○ Sixteen cells
- ◉ Thirty two cells
- ○ Sixty four cells

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Question # 29 of 30 (Start time: 05:17:21 PM, 28 December 2020)

1011+101 = _____

Select the correct option

- (●) 10000
- ( ) 00001
- ( ) 10011
- ( ) 11001

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Question # 30 of 30 (Start time: 05:17:51 PM, 28 December 2020)

Consider the sum of weight method for converting decimal into binary value, _____ is the highest weight for 411.

Select the correct option

- ○ 64
- ○ 128
- ◉ 256
- ○ 512

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Quiz Start Time: (

Question # 1 of 30 (Start time: 06:21:30 PM, 28 December 2020)

The series of TTL chips are characterized by their _____.

Select the correct option

- ○ Switching Speed Only
- ○ Power Dissipation Only
- ◉ Both
- ○ None of above

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Quiz Start Time: 06:21 PM, 28 December 2

Question # 2 of 30 (Start time: 06:22:07 PM, 28 December 2020)

Total Marl

The 3-to-8 Decoder has active-low outputs and three extra ____ gates connected at the three inputs to reduce the four unit load to a single unit load.

Select the correct option

- AND
- OR
- NOT
- NOR

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Quiz Start Time: 06:21 PM, 2

Question # 19 of 30 (Start time: 06:29:01 PM, 28 December 2020)

_____ is used when the output is connected back to the input of the PAL or if the output pin is used as an input only.

Select the correct option

- (●) Combinational Input/Output
- ( ) Combinational Output
- ( ) Combinational Input
- ( ) Programmable polarity

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Question # 20 of 30 (Start time: 06:29:34 PM, 28 December 2020)

A.(B + C) = A.B + A.C is the expression of _____

Select the correct option

- ○ Demorgan's Law
- ○ Commutative Law
- ◉ Distributive Law
- ○ Associative Law

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Quiz Start Time: 06:21 P

Question # 21 of 30 (Start time: 06:29:59 PM, 28 December 2020)

GAL can be reprogrammed as instead of fuses E2CMOS logic is used which can be programmed to connect a _____ with a _____.

Select the correct option

- (●) column,row
- ( ) row,column
- ( ) column,column
- ( ) row,row

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Click to Save Answer & Mov

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Question # 22 of 30 (Start time: 06:30:21 PM, 28 December 2020)

NAND gate is form by connecting _____

Select the correct option

- [x] AND Gate and then NOT Gate
- [ ] NOT Gate and then AND Gate
- [ ] AND Gate and then OR Gate
- [ ] OR Gate and then AND Gate

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Question # 23 of 30 (Start time: 06:30:37 PM, 28 December 2020)

In 8-input multiplexer, the two outputs are connected through a/an _____ gate.

Select the correct option

- ○ AND
- ◉ OR
- ○ NOT
- ○ NOR

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Quiz Start Time: 06:21 PM, 28 D

Question # 24 of 30 (Start time: 06:31:07 PM, 28 December 2020)

In 16-bit ALU, the Carry outputs C1, C2 and C3 from the Look-Ahead Carry generator circuit are generated after a gate delay of _____.

Select the correct option

- ○ 1
- ◉ 2
- ○ 3
- ○ 4

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Question # 25 of 30 (Start time: 06:31:36 PM, 28 December 2020)

In 16-bit ALU, the G output is activated if the 4-bit unit generates a _____ irrespective of _____.

Select the correct option

- (●) Carry out, carry in
- ( ) Carry in, carry out
- ( ) Carry in, input number
- ( ) Input number, carry in

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Question # 26 of 30 (Start time: 06:32:01 PM, 28 December 2020)

The sum terms in Standard POS form are called _____

Select the correct option

- Midterms
- Minterms
- Maxterms
- Mixterms

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Quiz Start Time

Question # 27 of 30 (Start time: 06:32:25 PM, 28 December 2020)

The limitation in implementation of parallel binary adders is known as _____.

Select the correct option

- ○ Delay.
- ◉ Carry propagation.
- ○ Carry input.
- ○ Carry output.

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Question # 28 of 30 (Start time: 06:32:48 PM, 28 December 2020)

Gray code of 8 is:

Select the correct option

- [x] 1100
- [ ] 1000
- [ ] 0000
- [ ] 0100

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Question # 29 of 30 (Start time: 06:33:12 PM, 28 December 2020)

The output of an AND gate is one when _____

Select the correct option

◉	All of the inputs are one
○	Any of the input is one
○	Any of the input is zero
○	All the inputs are zero

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Question # 30 of 30 (Start time: 06:33:33 PM, 28 December 2020)

Don't care conditions are marked as _____ in the output column of the function table

Select the correct option

- 0
- 1
- X
- A

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