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WARNING: Team HADI is not responsible for any mistake or wrong answer. All students reading and using this document may check and confirm the answers at their own.

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Best of luck!

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TEAM HADI (VU)

CS501 - Advance Computer Architecture (Quiz No 02)

Quiz Start Time: 09:36 AM, 28 November 2023

Question # 5 of 10 (Start time: 09:39:56 AM, 28 November 2023)

Total Marks: 1

The _____ control signal is used to enable the tri-state buffers with the MBR.

Select the correct option

- (●) MBRout
- ( ) MARout
- ( ) LMAR
- ( ) LMBR

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CS501 - Advance Computer Architecture (Quiz No. 1)

Quiz Start Time: 12:44 PM, 17 November 2023

Question # 8 of 10 (Start time: 12:49:25 PM, 17 November 2023)

Total Marks: 1

_____ is an example of Miscellaneous instruction.

Select the correct option

- Shiftl
- Halt
- Call
- Store

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CS501 - Advance Computer Architecture (Quiz No. 1) **Quiz Start Time: 12:44 PM, 17 November 2023**

Question # 3 of 10 (Start time: 12:46:32 PM, 17 November 2023) **Total Marks:** 1

Which type of instructions enables mathematical computations?

Select the correct option

- ○ Numeric
- ◉ Arithmetic
- ○ Control
- ○ Data transfer

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CS501 - Advance Computer Architecture (Quiz No. 1) **Quiz Start Time: 12:44 PM, 17 November 2023**

Question # 6 of 10 (Start time: 12:48:12 PM, 17 November 2023) **Total Marks:** 1

In _____ instruction format of EAGLE processor, there is no field reserved for operands.

Select the correct option

- (●) Type Z
- ( ) Type Y
- ( ) Type V
- ( ) Type X

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CS501 - Advance Computer Architecture (Quiz No 02) **Quiz Start Time: 09:36 AM, 28 November 2023**

Question # 7 of 10 (Start time: 09:41:16 AM, 28 November 2023) **Total Marks:** 1

The multiplexer_____ is used to decide which value is transferred to be written back to the register file.

Select the correct option

- MP2
- MP4
- MP5
- MP3

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CS501 - Advance Computer Architecture (Quiz No 02) **Quiz Start Time: 09:36 AM, 28 November 2023**

Question # 2 of 10 (Start time: 09:37:20 AM, 28 November 2023) **Total Marks:** 1

Which are the two control unit design approaches?

Select the correct option

- ○ Control signals and busses
- ○ Registers and instructions
- ◉ Hardwired and micro programmed
- ○ Cache and RAM

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CS501 - Advance Computer Architecture (Quiz No. 1)

Quiz Start Time: 12:44 PM, 17 November 2023

Question # 9 of 10 (Start time: 12:49:45 PM, 17 November 2023)

Total Marks: 1

Which one of the following is a bi-stable device, capable of storing one bit of Information?

Select the correct option

- Multiplexer
- Decoder
- Diplexer
- Flip-flop

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CS501 - Advance Computer Architecture (Quiz No. 1) **Quiz Start Time: 12:44 PM, 17 November 2023**

Question # 10 of 10 (Start time: 12:50:51 PM, 17 November 2023) **Total Marks:** 1

A general purpose digital computer has _____ main components.

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Select the correct option

- 4
- 3
- 5
- 2

CS501 - Advance Computer Architecture (Quiz No. 1) Quiz Start Time: 12:44 PM, 17 November 2023

Question # 5 of 10 (Start time: 12:47:46 PM, 17 November 2023) Total Marks: 1

Which operator is used to 'name' registers, or part of registers, in the Register Transfer Language?

Select the correct option

- %
- &
- ©
- :=

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CS501 - Advance Computer Architecture (Quiz No 02)

Quiz Start Time: 09:36 AM, 28 November 2023

Question # 9 of 10 (Start time: 09:42:47 AM, 28 November 2023)

Total Marks: 1

Pipeline hazard occurs when an instruction depends on the result of _____ instruction that is not yet complete.

Select the correct option

- ○ first
- ◉ previous
- ○ ongoing
- ○ next

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CS501 - Advance Computer Architecture (Quiz No 02) **Quiz Start Time: 09:36 AM, 28 November 2023**

Question # 6 of 10 (Start time: 09:40:45 AM, 28 November 2023) **Total Marks:** 1

In case of SRC processor, bits _____ of IR (instruction register) are reserved for the op-code.

Select the correct option

- 59 to 63
- 11 to 15
- 0 to 4
- 27 to 31

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CS501 - Advance Computer Architecture (Quiz No 02)

Quiz Start Time: 09:36 AM, 28 November 2023

Question # 1 of 10 (Start time: 09:36:49 AM, 28 November 2023)

Total Marks: 1

During the RESET operation of processor, control step counter is set to _____.

Select the correct option

- 1
- 0
- -1
- 2

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CS501 - Advance Computer Architecture (Quiz No. 1) **Quiz Start Time: 12:44 PM, 17 November 2023**

Question # 7 of 10 (Start time: 12:48:37 PM, 17 November 2023) **Total Marks:** 1

Type checking allows the _____ to determine memory requirements for variables.

Select the correct option

- ○ Debugger
- ○ Loader
- ○ Linker
- ◉ Compiler

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CS501 - Advance Computer Architecture (Quiz No 02) **Quiz Start Time: 09:36 AM, 28 November 2023**

Question # 3 of 10 (Start time: 09:38:39 AM, 28 November 2023) **Total Marks:** 1

Which one of the following registers stores a previously calculated value or a value loaded from the main memory?

Select the correct option

- (●) Accumulator
- ( ) Instruction Register
- ( ) Program Counter
- ( ) Address Mask

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CS501 - Advance Computer Architecture (Quiz No 02)

Quiz Start Time: 09:36 AM, 28 November 2023

Question # 4 of 10 (Start time: 09:39:18 AM, 28 November 2023)

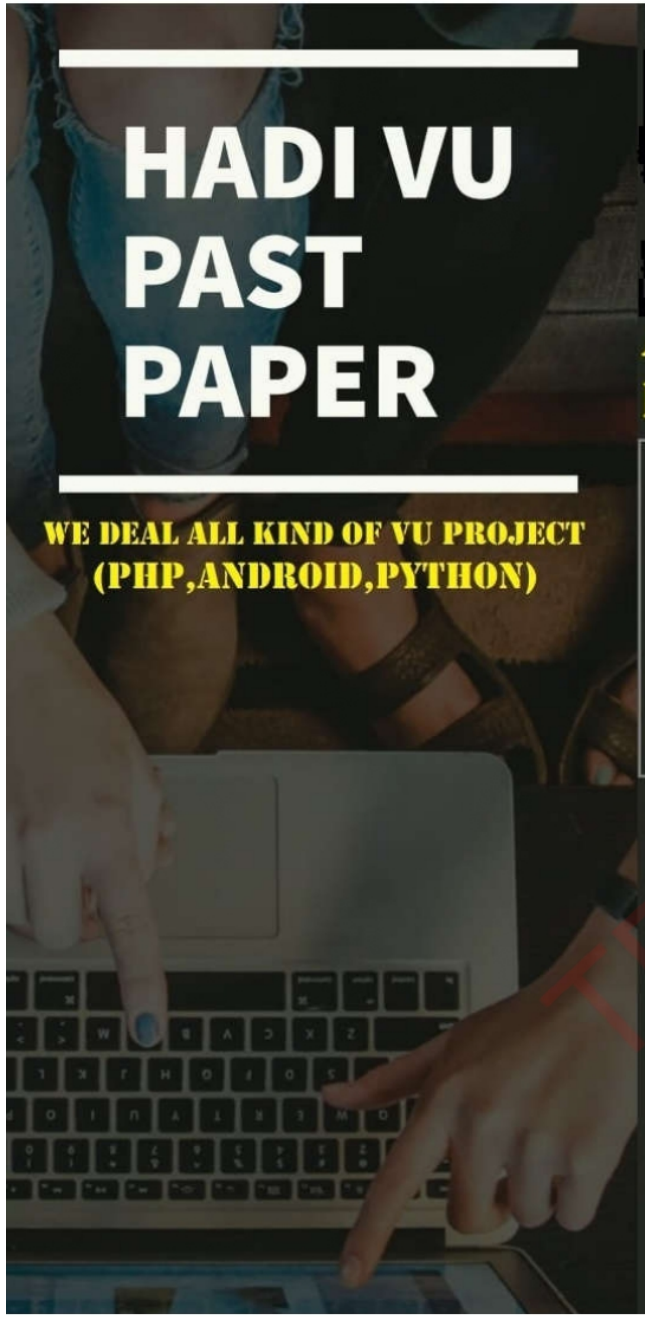
Total Marks:

The_____ control signal enables the input to the register C for writing the incremented value of PC onto it.

Select the correct option

- LC
- LPC
- PCout
- INC4

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