

VIRTUAL UNIVERSITY OF PAKISTAN

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CORRECT ANSWER SOLVED BY HADI
EMAIL: hadirajputofficial@gmail.com
WHATSAPP & CALL: 03087122922

(Final Term Past Paper)

MADE AND SOLVED BY TEAM HADI

WARNING: Team HADI is not responsible for any mistake or wrong answer. All students reading and using this document may check and confirm the answers at their own.



WhatsApp: 03087122922



FACEBOOK ID: <https://www.facebook.com/hadipastpapers/>

Best of luck!



Question # 10 of 10 (Start time: 02:19:51 PM, 13 February 2018)

Total Marks: 1

Cluster number can also be referred as block number.

Select the correct option

☐	True
☒	False

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CELL NO: 03228043306
EMAIL: HADIRAJPUTOFFICIAL@GMAIL.COM

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Click to Save Answer & Move to Next Question

Question # 3 of 10 (Start time: 02:11:58 PM, 13 February 2018)

Total Marks: 1

To store a cluster in FAT 32 _____ is/are needed.

Select the correct option

☐	Nibble	
☐	Byte	
☐	2 Bytes	
☒	4 Bytes	

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EMAIL: HADIRAJPUTOFFICIAL@GMAIL.COM

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Question # 5 of 10 (Start time: 02:50:10 PM, 13 February 2018)

Total Marks: 1

If we want to produce the shrill voice from the speaker phone then we have to load the ___ divisor value at Port ___.

Select the correct option

☐	high, 0x42	
☐	low, 0x42	
☒	high, 0x22	
☐	low, 0x22	

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Question # 6 of 10 (Start time: 02:15:32 PM, 13 February 2018)

Total Marks: 1

The _____ function uses the COM port number to receive a byte from the COM port using BIOS services.

Select the correct option

☐	initialize()	h
☐	recievebyte()	h
☐	recieve()	h
☒	recievechar()	h

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Question # 7 of 10 (Start time: 02:16:52 PM, 13 February 2018)

Total Marks: 1

In FAT32, lower _____ bits are used.

Select the correct option

☐	26
☒	28
☐	30
☐	32

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Question # 8 of 10 (Start time: 02:17:57 PM, 13 February 2018)

Total Marks: 1

Int86() has _____ parameters.

Select the correct option

☐	0	
☐	1	
☐	2	
☒	3	

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Question # 8 of 10 (Start time: 02:52:22 PM, 13 February 2018)

Total Marks: 1

Int 14H _____ can be used to set the line parameter of the UART or COM port.

Select the correct option

☒	Service # 0	✓
☐	Service # 1	✓
☐	Service # 2	✓
☐	None of the given options	✓

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EMAIL: HADIRAJPUTOFFICIAL@GMAIL.COM

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[Click to Go to Answer & Move to Next Question](#)

Question # 7 of 10 (Start time: 02:51:57 PM, 13 February 2018)

Total Marks: 1

In counter register bit no. 3 changes its value between 0 and 1 with in ____clock cycles

Select the correct option

☐	1	
☐	2	
☐	4	
☒	16	

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Question # 9 of 10 (Start time: 02:53:16 PM, 13 February 2018)

Total Marks: 1

Only _____ ports are important from programming point of view.

Select the correct option

☒	70 and 71H	
☐	71 and 72H	
☐	70 and 72H	
☐	72 and 73H	

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EMAIL: HADIRAJPUTOFFICIAL@GMAIL.COM

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Question # 5 of 10 (Start time: 02:14:10 PM, 13 February 2018)

Total Marks: 1

if the following function,
keep(1, 1000);
is called in the TSR program, it means that _____ No. of bytes are reserved in the memory.

CHECK

Select the correct option

☒	8000	CORRECT ANSWER SOLVED BY HADI CELL NO: 03228043306 EMAIL: HADIRAJPUTOFFICIAL@GMAIL.COM	1/1
☐	16000		1/1
☐	32000		1/1
☐	64000		1/1

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Total size of partition table is _____ bytes.

Select the correct option

☐	64	
☐	128	
☐	256	
☒	512	

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EMAIL: HADIRAJPUTOFFICIAL@GMAIL.COM

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Question # 5 of 10 (Start time: 02:50:10 PM, 13 February 2018)

Total Marks: 1

If we want to produce the shrill voice from the speaker phone then we have to load the ____ divisor value at Port ____.

Select the correct option

☐	high, 0x42	
☐	low, 0x42	
☒	high, 0x22	
☐	low, 0x22	

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CELL NO: 03228043306
EMAIL: HADIRAJPUTOFFICIAL@GMAIL.COM

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Question # 3 of 10 (Start time: 06:00:24 PM, 20 July 2020)

Total Marks:

The value _____ of partition table signature indicate that the code part contains valid executable code.

Select the correct option

☐	AA22
☐	AA33
☐	AA44
☒	AA55

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Question # 5 of 10 (Start time: 06:03:50 PM, 20 July 2020)

Total Marks: 1

Highest physical capacity of the disk according to the IDE interface is approximately _____

Select the correct option

☐	504MB
☐	80GB
☒	127GB
☐	256GB

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Question # 10 of 10 (Start time: 06:11:05 PM, 20 July 2020)

Total Marks: 1

If 16 bits values are to be transmitted then _____ DMA controllers are required.

Select the correct option

☒	Two	
☐	Three	
☐	Four	
☐	Eight	

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Question # 6 of 10 (Start time: 06:04:48 PM, 20 July 2020)

Total Marks: 1

_____ will specify if the DMA transfer will happen as a single transfer, block transfer or demand transfer.

Select the correct option

☐	DMA Request register
☐	DMA Mask register
☐	DMA Mode register
☒	DMA Command register

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Question # 7 of 10 (Start time: 06:06:17 PM, 20 July 2020)

Total Marks: 1

Total size of partition table is _____ bytes.

Select the correct option

☐	64
☐	128
☐	256
☒	512

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Question # 8 of 10 (Start time: 06:07:29 PM, 20 July 2020)

Total Marks: 1

A single DMA can transfer _____ bit operand to and from memory in one bus cycle.

Select the correct option

☐	4
☒	8
☐	12
☐	16

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